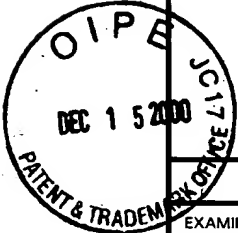


Subst. Form PTO-1449				Docket Number (Optional) SAM-169		Application Number 09/714,325	
INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>				Applicant Sung-Bae Park			
				Filing Date November 15, 2000		Group Art Unit	
U. S. Patent Documents							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AG	AA	5,381,533	1/10/95	Peleg, et. al.	395	375	3/30/94
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES NO
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
AG	AR	James E. Smith and Sriram Vajapeyam, "Trace Processors: Moving to Fourth-Generation Microarchitecture," September 1997 IEEE, pp.68-74.					
AG	AS	John D. Johnson, "Expansion Caches for Superscalar Processors," June 1994 Technical Report No. CSL-TR-94-630					
AG	AT	Eric Rotenberg, Steve Bennett, and Kames E. Smith, "Trace Cache: a Low Latency Approach to High Bandwidth Instruction Fetching," 1996 IEEE, pp. 24-34.					
EXAMINER Amol Gole				DATE CONSIDERED 10/15/03			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy with next communication to applicant.							



Subst. Form PTO-1449				Docket Number (Optional) SAM-169		Application Number 09/714,325	
INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>				Applicant Sung-Bae Park			
				Filing Date November 15, 2000		Group Art Unit	
U. S. Patent Documents							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	BA						
	BB						
	BC						
	BD						
	BE						
	BF						
	BG						
	BH						
	BI						
	BJ						
	BK						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES NO
	BL						
	BM						
	BN						
	BO						
	BP						
	BQ						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
<i>AG</i>	BR	Sanjay Jeram Patel, Marius Evers and Yale N. Patt, "Improving Trace Cache Effectiveness with Branch Promotion and Trace Packing," 1998 IEEE, pp. 262-271					
<i>AG</i>	BS	Sanjay Jeram Patel Daniel Holmes Friendly, and Yale N. Patt, "Evaluation of Design Options for the Trace Cache Fetch Mechanism," 1999 IEEE, pp. 193-204					
<i>AG</i>	BT	Eric Rotenberg, Steve Bennett and James E. Smith, "A Trace Cache Microarchitecture and Evaluation," IEEE 1999, pp. 111-120					
EXAMINER <i>Amel Gold</i>				DATE CONSIDERED 10/15/03			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy with next communication to applicant.							